



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	ON NO. FILING DATE FIRST NAMED INVENTOR		ATTORNEY DOCKET NO. CONFIRMATION		
09/823,079	03/30/2001	Errol C. Heiman	STL9524	6981	
75	90 05/19/2004	EXAMINER			
	eagate Technology LLC	LEROUX, ETIENNE PIERRE			
Intellectual Prop	perty Department	ART UNIT	PAPER NUMBER		
Shakopee, MN 55379			2171	19	
		DATE MAILED: 05/19/2004			

Please find below and/or attached an Office communication concerning this application or proceeding.

h

·		· · · · · · · · · · · · · · · · · · ·							
Office Action Summany		Application	No.	Applicant(s)	Λ				
		09/823,079		HEIMAN ET AL.					
Office Action Summary			Examiner		Art Unit	•			
		Etienne P Le		2171					
Period fo	Th MAILING DATE of this commu or Reply	inication appe	ears on the c	ov rshe t with th	correspondence address	S			
THE I - Exter after - If the - If NO - Failu - Any r	ORTENED STATUTORY PERIOD MAILING DATE OF THIS COMMUI nsions of time may be available under the provision SIX (6) MONTHS from the mailing date of this conperiod for reply specified above is less than thirty period for reply is specified above, the maximum re to reply within the set or extended period for reply received by the Office later than three months and patent term adjustment. See 37 CFR 1.704(b).	NICATION. ns of 37 CFR 1.136 nmunication. (30) days, a reply statutory period wi bly will, by statute, o	6(a). In no event, within the statutor ill apply and will e cause the applica	however, may a reply be t y minimum of thirty (30) da xpire SIX (6) MONTHS fron tion to become ABANDON	imely filed ays will be considered timely. m the mailing date of this commun IED (35 U.S.C.§ 133).	nication.			
1)⊠	Responsive to communication(s) fi	led on <u>16 Ap</u>	<u>ril 2004</u> .						
2a) <u></u> ☐	This action is FINAL .	2b)⊠ This a	action is non-	final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.								
Dispositi	on of Claims								
4)⊠	Claim(s) 18-28 is/are pending in th	e application	ı .						
-	4a) Of the above claim(s) is/are withdrawn from consideration.								
5)□	S) Claim(s) is/are allowed.								
6)⊠	i)⊠ Claim(s) <u>18-28</u> is/are rejected.								
7)	Claim(s) is/are objected to.								
8)□	Claim(s) are subject to restr	iction and/or	election req	uirement.					
Applicati	on Papers								
9)[The specification is objected to by t	he Examiner							
10)🏻	The drawing(s) filed on <u>30 March 2</u>	<u>001</u> is/are: a	ı)⊠ accepte	d or b)□ objected	to by the Examiner.				
	Applicant may not request that any obj	ection to the d	lrawing(s) be	neld in abeyance. Se	ee 37 CFR 1.85(a).				
	Replacement drawing sheet(s) including	-	•	,	•	• •			
11)	The oath or declaration is objected	to by the Exa	aminer. Note	the attached Offic	e Action or form PTO-19	52.			
Priority ι	ınder 35 U.S.C. §§ 119 and 120			•					
* S 13)⊠ A si 3 a 14)□ A	Acknowledgment is made of a claim All b) Some * c) None of: 1. Certified copies of the priorit 2. Certified copies of the priorit 3. Copies of the certified copies application from the Internat See the attached detailed Office act acknowledgment is made of a claim nce a specific reference was included T CFR 1.78. 1. The translation of the foreign is acknowledgment is made of a claim acknowledgment is made o	y documents y documents s of the priori ional Bureau ion for a list o for domestic led in the first anguage prov for domestic	have been in the have been in the certified priority under the sentence of the certified priority under	received. received in Applica s have been received. 17.2(a)). d copies not receiver 35 U.S.C. § 119 f the specification of the specification receiver 35 U.S.C. §§ 12	tion No yed in this National Stag yed. (e) (to a provisional app or in an Application Data eceived. 0 and/or 121 since a sp	olication) a Sheet. ecific			
Attachmen	• •		4.	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	(DTO 442) B N ()				
2) Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review mation Disclosure Statement(s) (PTO-1449)				y (PTO-413) Paper No(s) Patent Application (PTO-152)				

Application/Control Number: 09/823,079 Page 2

Art Unit: 2171

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the

basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 18-20, 27-30 and 37 are rejected under 35 U.S.C. 102(e) as being anticipated by

Pub No US 2002/0050813 issued to Yamamoto (hereafter Yamamoto).

Claims 18 and 28:

Yamamoto discloses a power tester apparatus for testing an electronic device, the device configured to operate using a constant power supply voltage, the apparatus comprising:

• a power source supplying the constant power supply voltage [Vcc, Fig 2]

a connector coupled to the power source, the connector adapted to connect the constant

power supply voltage to a power supply input on the electronic device [Fig 1, 7];

circuitry configured to introduce disturbances into the constant power supply voltage

applied to the electronic device, a disturbance configured to simulate an unexpected

change in a nominal power supply voltage, wherein the disturbances introduced into the

constant power supply voltage applied to the electronic device are controllable [Fig 1,

15].

Claims 19 and 29:

Application/Control Number: 09/823,079 Page 3

Art Unit: 2171

Yamamoto discloses wherein the disturbance is a rising pulse having a maximum voltage which is controllable [Fig 2].

Claims 20 and 30:

Yamamoto discloses wherein the disturbance is a low-going pulse having a minimum voltage being less than the voltage [Fig 2].

Claim 27:

Yamamoto discloses wherein the disturbance comprises a voltage sequence applied during powering up of the electronic device [abstract].

Claim 37:

Yamamoto discloses providing a 0 VDC voltage for a preselected duration of time after the voltage is coupled to the connector [Fig 2].

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 21 and 31 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamamoto in view of US Pat No 5,353,254 issued to Sakamoto (hereafter Sakamoto).

Claims 21 and 31:

Yamamoto discloses the elements of claims 18 and 28 as noted above.

Yamamoto fails to disclose wherein the constant power supply voltage is selected from the group of voltages consisting of +5 VDC and +12 VDC.

Sakamoto discloses as admitted prior art wherein the constant power supply voltage is selected from the group of voltages consisting of +5 VDC and +12 VDC [col 1, lines 13-20].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Yamamoto to include wherein the constant power supply voltage is selected from the group of voltages consisting of +5 VDC and +12 VDC as taught by Sakamoto.

The ordinarily skilled artisan would have been motivated to modify Yamamoto per the above for the purpose of selecting a conventional power source [col 1, lines 13-20].

Claims 22, 23, 32 and 33 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamamoto in view of US Pat No 5,949,671 issued to Lee et al (hereafter Lee '671).

Claims 22 and 32:

Yamamoto discloses the elements of claims 18 and 28 as noted above,

Yamamoto fails to disclose further comprising an additional power source supplying an additional voltage wherein the additional power source is adapted to connect the additional voltage to an additional connector.

Lee '671 discloses a dual voltage power supply [Fig 1, 130, 140].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Yamamoto to include a dual voltage power supply as taught by Lee '671.

The ordinarily skilled artisan would have been motivated to modify Yamamoto per the above for the purpose of providing a voltage supply suitable for powering the electronic device at the design voltage of the electronic device.

Furthermore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the combination of Yamamoto and Lee '671 to include further comprising an additional power source supplying an additional voltage wherein the additional power source is adapted to connect the additional voltage to an additional connector.

The ordinarily skilled artisan would have been motivated to modify the combination of Yamamoto and Lee '671 per the above for the purpose of physically providing a first voltage to a first device under test and a second voltage to a second device under test.

Claims 23 and 33:

The combination of Yamamoto and Lee '671 disclose the elements of claims 18, 22, 28 and 32 as noted above.

Lee '671 discloses 24 VDC [col 1, lines 55-60].

Claims 24-26 and 34-36 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamamoto in view of US Pat No 4,764,652 issued to Lee et al (hereafter Lee '652).

Claims 24 and 34:

Yamamoto discloses the elements of claims 18 and 28 as noted above.

Yamamoto fails to disclose a manually operated user interface used to control the disturbances.

Lee '652 discloses a manually operated user interface used to control the disturbances [Fig 3, 54].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Yamamoto to include a manually operated user interface used to control the disturbances as taught by Lee '652.

The ordinarily skilled artisan would have been motivated to modify Yamamoto per the above for the purpose of controlling the output to the work coil in order to control temperature of the cooker [Fig 1].

Claims 25, 26, 35 and 36

Yamamoto discloses the elements of claims 18 and 28 as noted above.

Yamamoto fails to disclose wherein the disturbance is at least one pulse having a duration and a magnitude which are controllable.

Lee '652 discloses wherein the disturbance is at least one pulse having a duration and a magnitude which are controllable [abstract].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Yamamoto to include wherein the disturbance is at least one pulse having a duration and a magnitude which are controllable as taught by Lee '652

The ordinarily skilled artisan would have been motivated to modify Yamamoto per the above for the purpose of controlling the output to the work coil in order to control temperature of the cooker [Fig 1].

Art Unit: 2171

Response to Arguments

Applicant's arguments filed 4/16/2004 have been fully considered but they are moot based on supra new grounds of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Etienne LeRoux whose telephone number is (703) 305-0620. The examiner can normally be reached on Monday – Friday from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Safet Metjahic, can be reached on (703) 308-1436.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Patent related correspondence can be forwarded via the following FAX number (703) 872-9306

Etienne LeRoux
5/14/2004